

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/961,218	BROWN ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Ajay M. Bhatia	2145	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
	O					
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	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	PDP-11 Assembler Language Programmin and Machine Oranization. Michael Sine, 1980, pages 118, 119, 141, 142
	V	IBM PC Assembly Language and Programming Fourth Edition, Peter Abel, 1998, pages 478, 479, 482-496
	W	Intel 82340 High-Performance ISA-compatible chip set data manual, for the 386 sm and 386 DX cpu, OCTober 1990, page 4-77
	X	MC88100 Risc Microprocessor Users' manual, Motorola, Aug 31, 1992, Section 4,

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.